

# EEHPC Session 5: Power API HPC Controls and Monitoring

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http://powerapi.sandia.gov



Exceptional

service

in the

national

interest

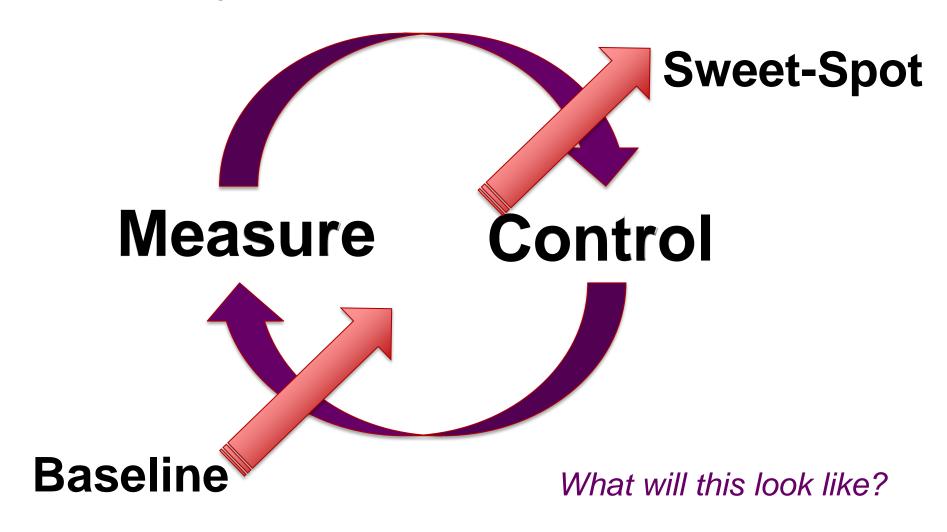


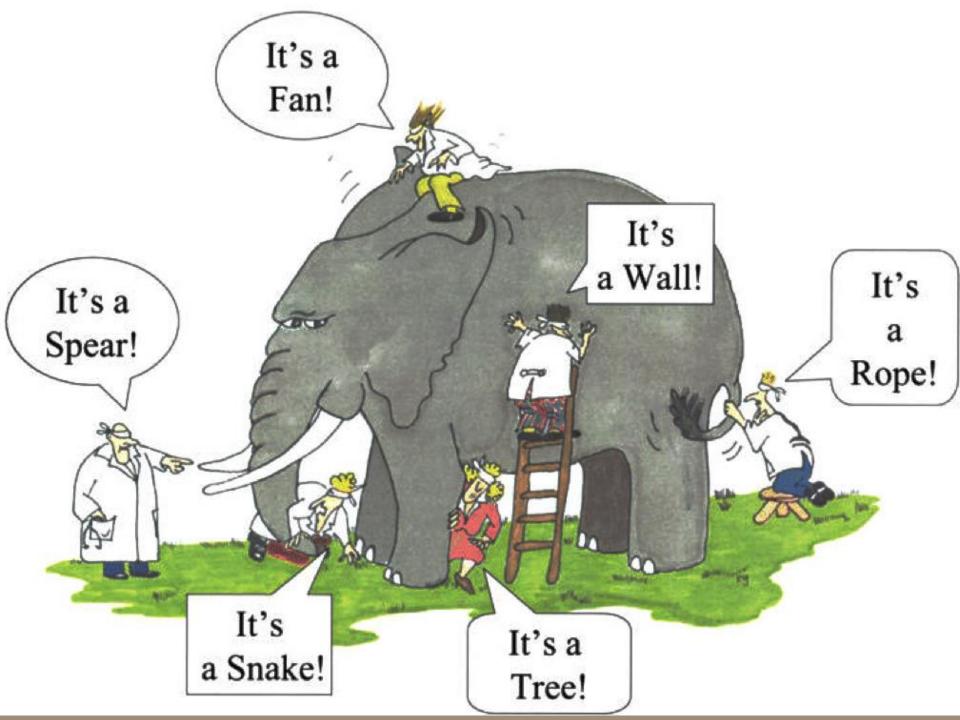


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# What Did We Learn? (from our early research)



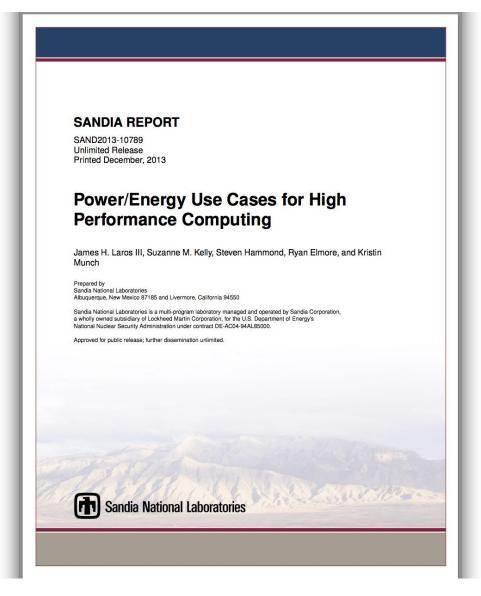






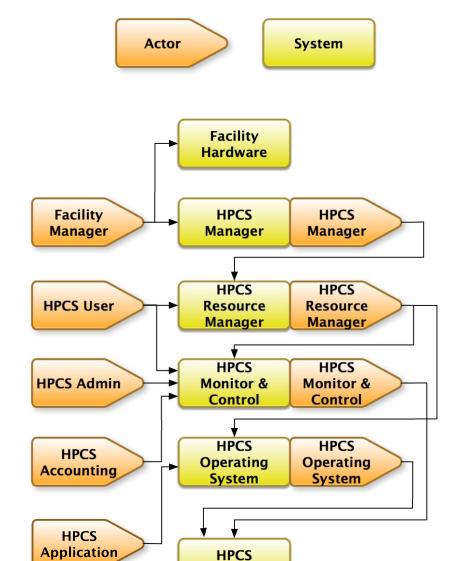
#### **Preliminary Investigation**

- Sandia and National Renewable Energy Laboratory (NREL)
  - Sandia Platform focused
  - NREL Facility focused
- Primary Objectives
  - Identify SCOPE of Power API
  - Identify Roles
  - Identify Interfaces
- Product:
  - "Power/Energy Use Cases for High Performance Computing"
  - Use Case Approach
  - Actor (Roles) -> System pairs
  - Interaction between Actor and System defines an Interface
  - Facility to Component (hardware) Scope



#### Result

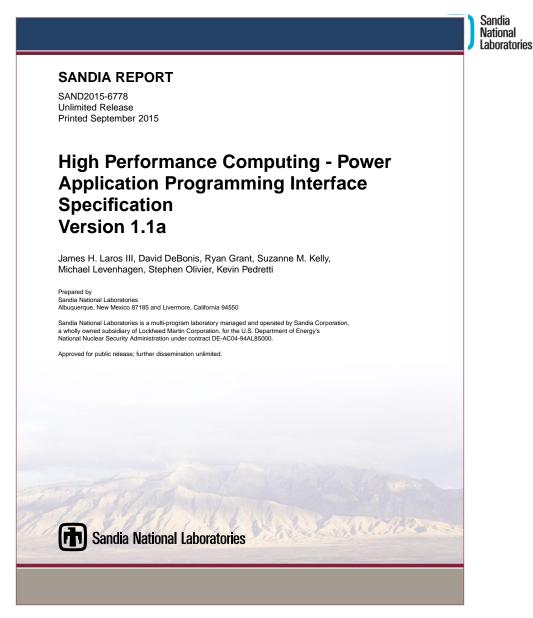




Hardware

#### **Version 1.1a Delivered**

- Community needed a portable API for measuring and controlling power and energy
- Sandia developed Power API specification to fill this gap
- Provides portable power
   measurement and control
   interfaces, covers full spectrum
   of facility to component
- First production implementation in progress on Trinity (ATS1)
- Continued (increasing)
   community involvement and
   influence



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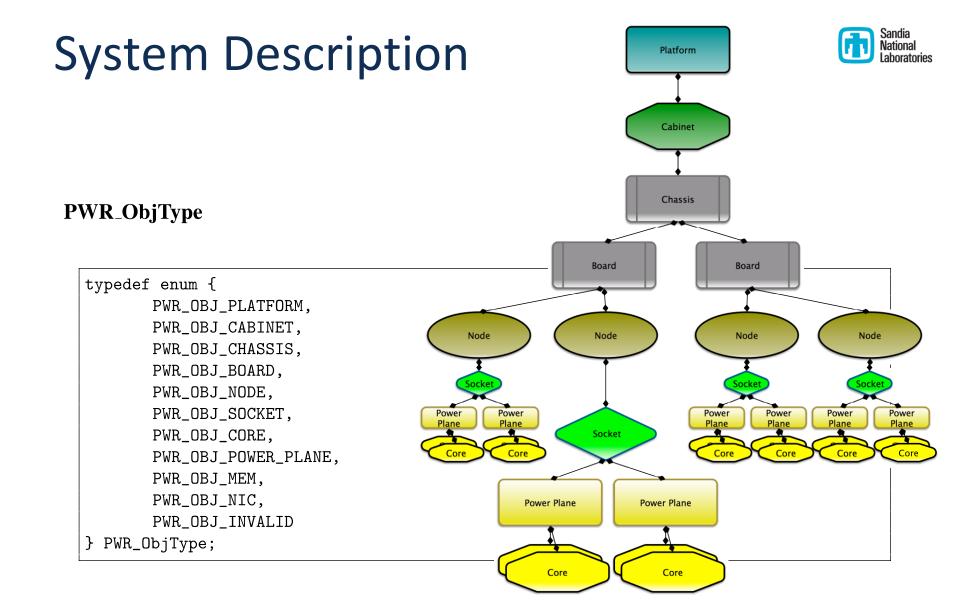
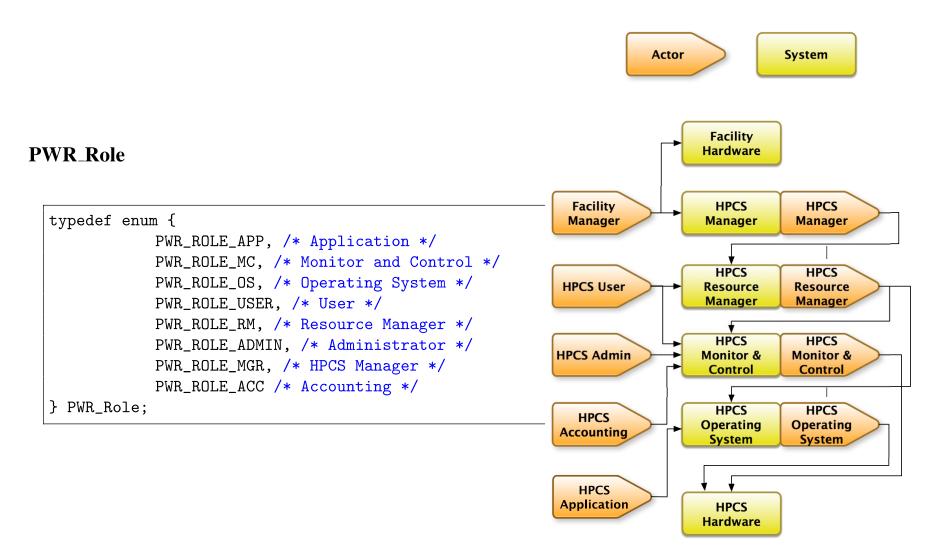


Figure 2.1: Hierarchical Depiction of System Objects

#### Roles





#### Foundation:

Measurement and Control



#### Sandia National

#### PWR\_AttrName

```
typedef enum {
     PWR_ATTR_PSTATE = 0, /* uint64_t */
     PWR_ATTR_CSTATE, /* uint64_t */
     PWR_ATTR_CSTATE_LIMIT, /* uint64_t */
     PWR_ATTR_SSTATE, /* uint64_t */
     PWR_ATTR_POWER, /* double, Watts */
     PWR_ATTR_CURRENT, /* double, Amps */
     PWR_ATTR_VOLTAGE, /* double, Voltage */
     PWR_ATTR_MAX_POWER, /* double, Watts */
     PWR_ATTR_MIN_POWER, /* double, Watts */
     PWR_ATTR_FREQ, /* double, Hz */
     PWR_ATTR_ENERGY, /* double, Joules */
     PWR_ATTR_TEMP, /* double, Celsius */
     PWR_ATTR_OS_ID, /* uint64_t */
     PWR_ATTR_NUM_ATTRS,
     PWR_ATTR_INVALID = PWR_ATTR_NUM_ATTRS,
} PWR AttrName;
```

#### Attribute Interface



#### **MEASURE**

#### **CONTROL**

#### Other Core Features

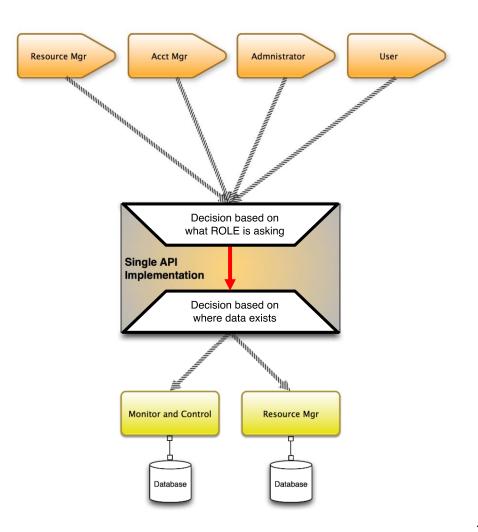


- Discovery and Navigation
  - Of system description
- Groups
  - Adds lots of flexibility and convenience
- Statistics
  - Real-time, historic and extensible
- Metadata
- Languages
  - V 1.0 1.1a Specified in C
  - V 1.x Python

## Higher Level Interfaces

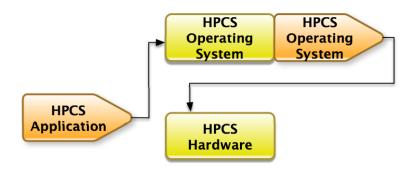


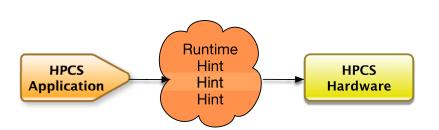
- Administrator:
  - Hard Power Capping
    - Bounds more granular Power Aware Scheduling
- Resource Manager:
  - Power Aware Scheduling
    - HPC Tetris
- Accounting Manager:
  - Power/Energy Application Profiles
    - Feed into Power Aware scheduling
- User:
  - Understanding application power and energy characteristics
    - Application Hints



# Higher Level Interfaces (cont.)



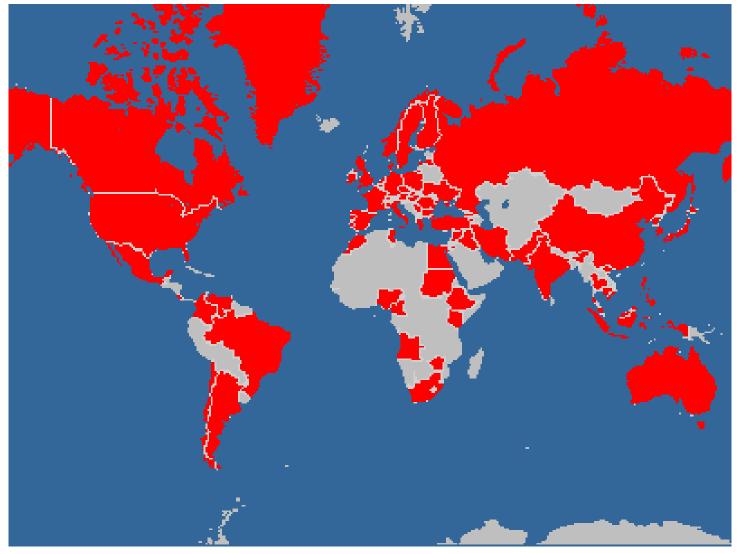




#### Application provides "hints"

- I'm going into a serial portion
  - Turn off cores
  - Accelerate active core
- Could be an intelligent dynamic run-time
- Research area





**81 Countries as of October 2015** 

### Questions?



- http://powerapi.sandia.gov
- Register on the reflector
- Get the current version of the spec
- Get the prototype source
- Other information as it develops
- Please get involved and help us improve the specification
- TEAM:

James Laros, Suzanne Kelly, Kevin Pedretti, Michael Levenhagen, Ryan Grant, Stephen Olivier, David DeBonis